

FIG. 1

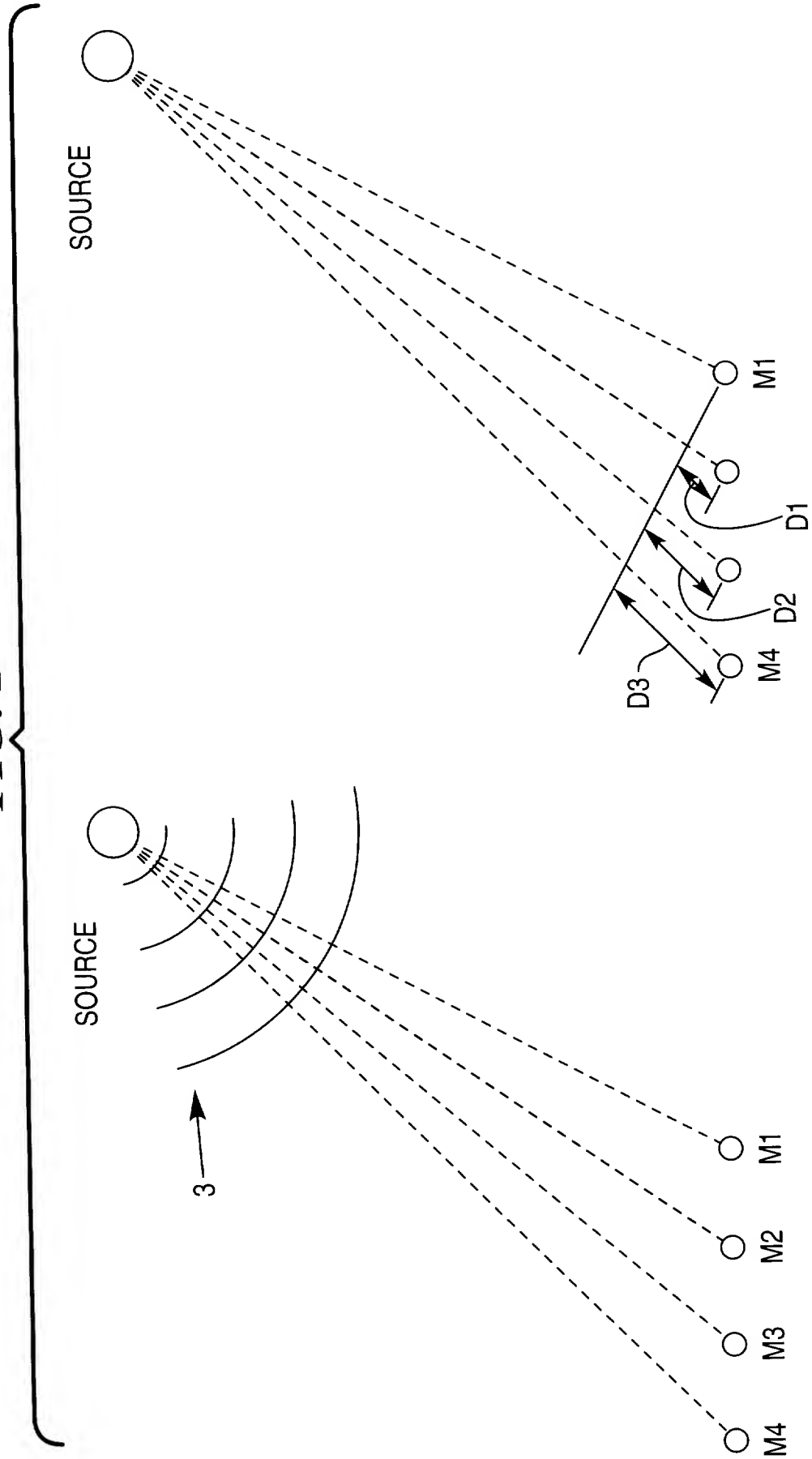


FIG. 2

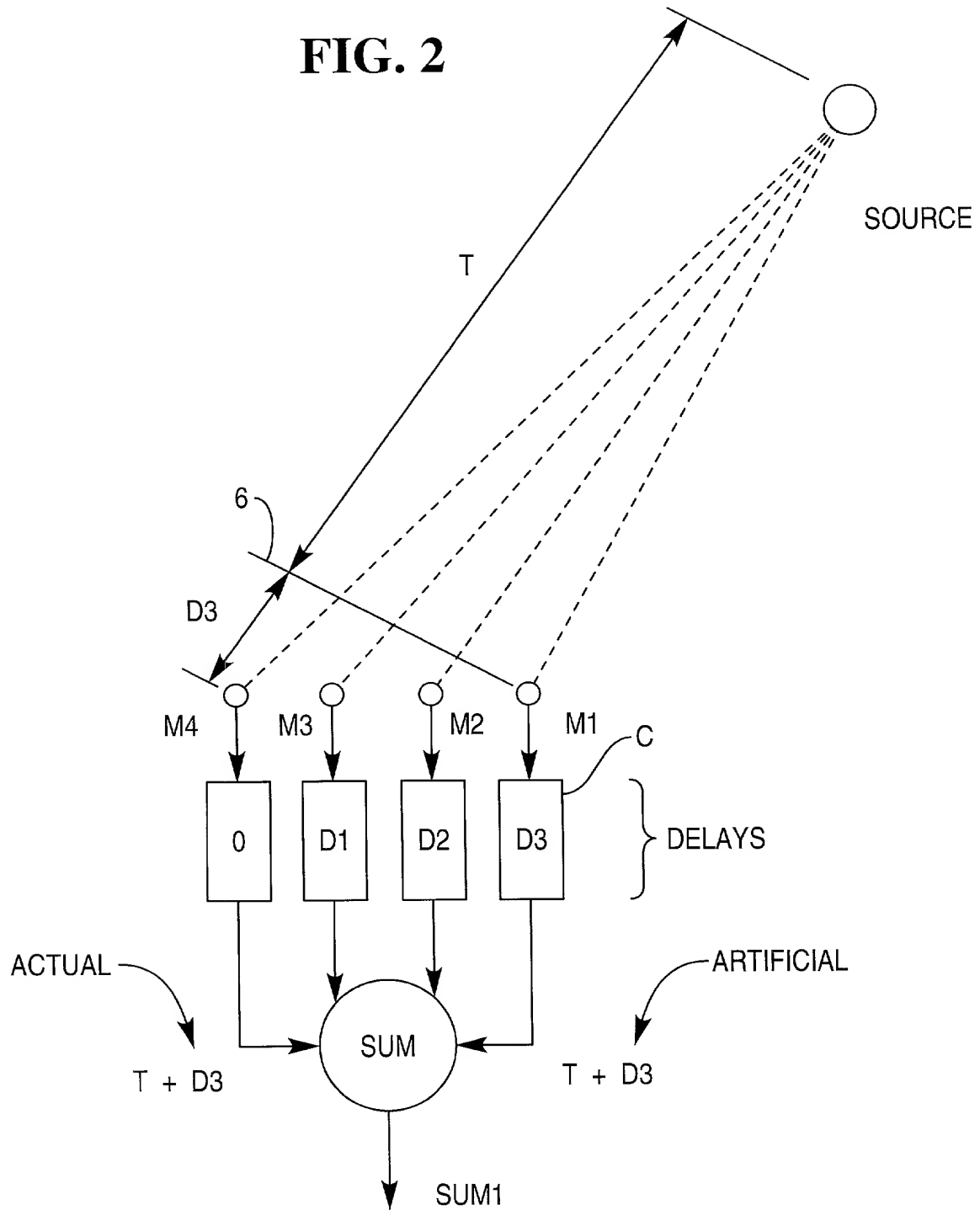


FIG. 3

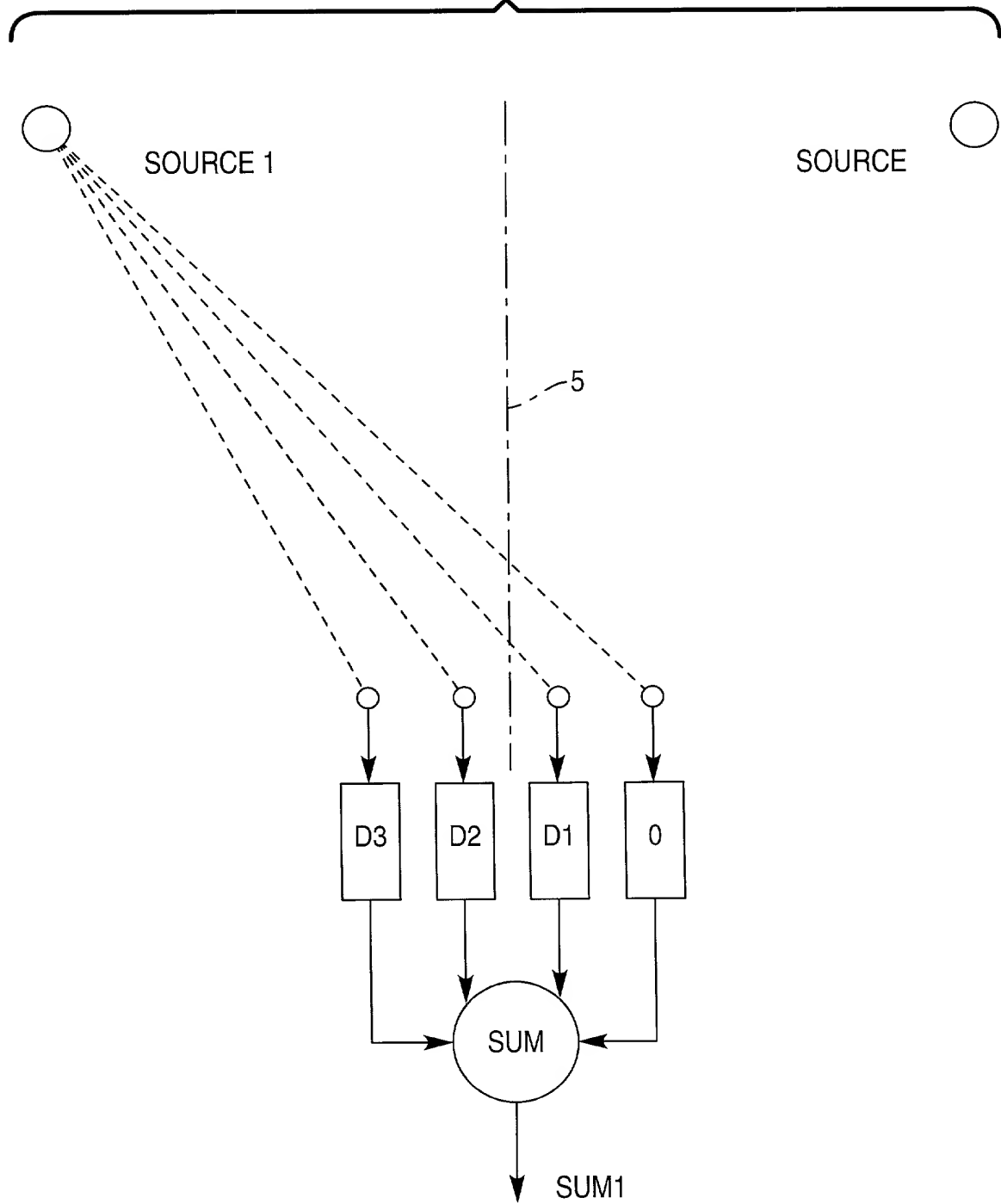
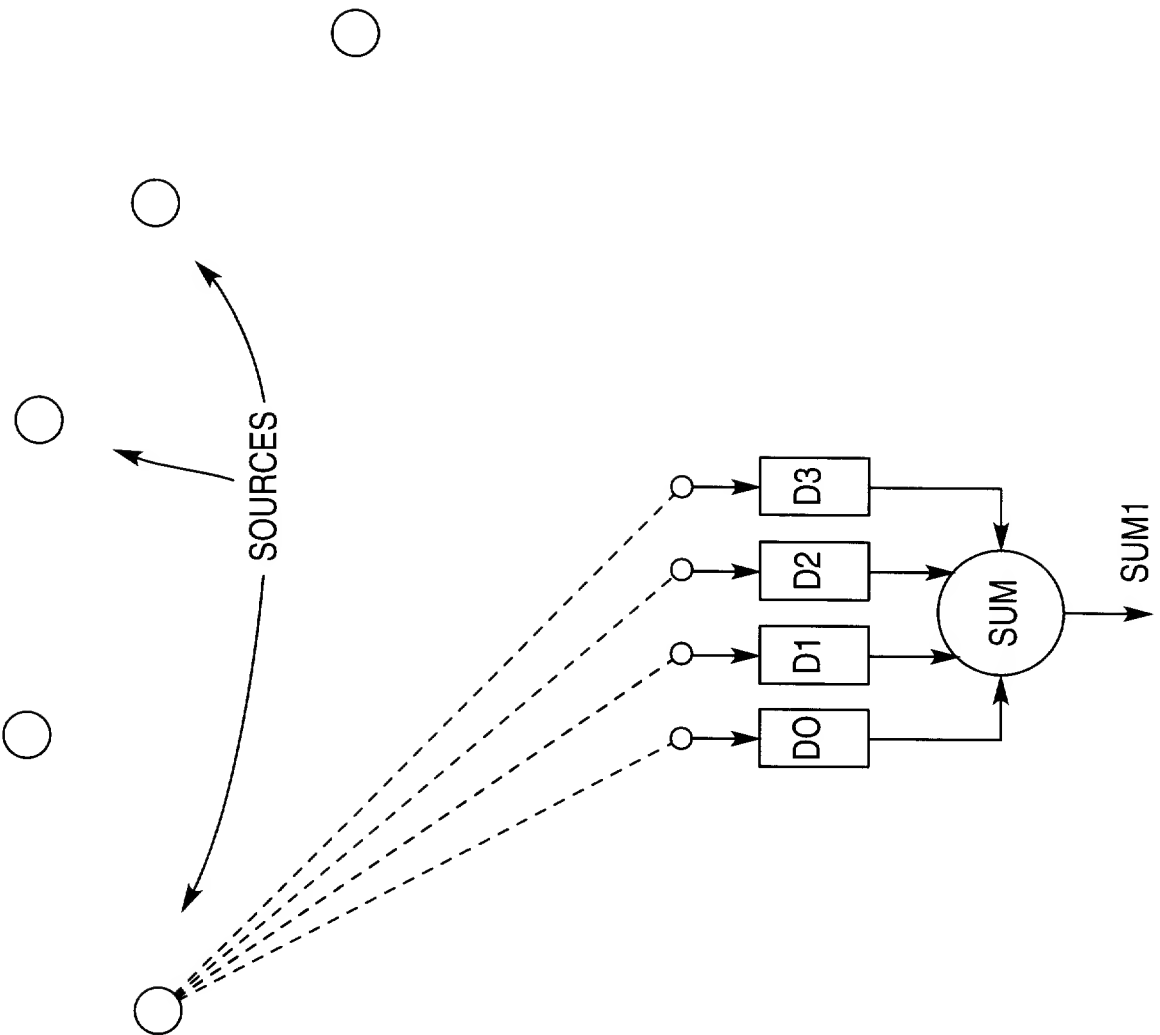


FIG. 4 is a schematic diagram of a beamforming system. The system includes a set of sources (represented by circles) and a set of delay sets (represented by circles). The sources are connected to the delay sets via dashed lines. The delay sets are connected to a summing junction (SUM) via solid lines. The summing junction outputs a signal labeled SUM1.



LOBE	DELAY SET
L1	1
L2	2
L3	3
L4	4
	etc.

TABLE

FIG. 4

FIG. 5

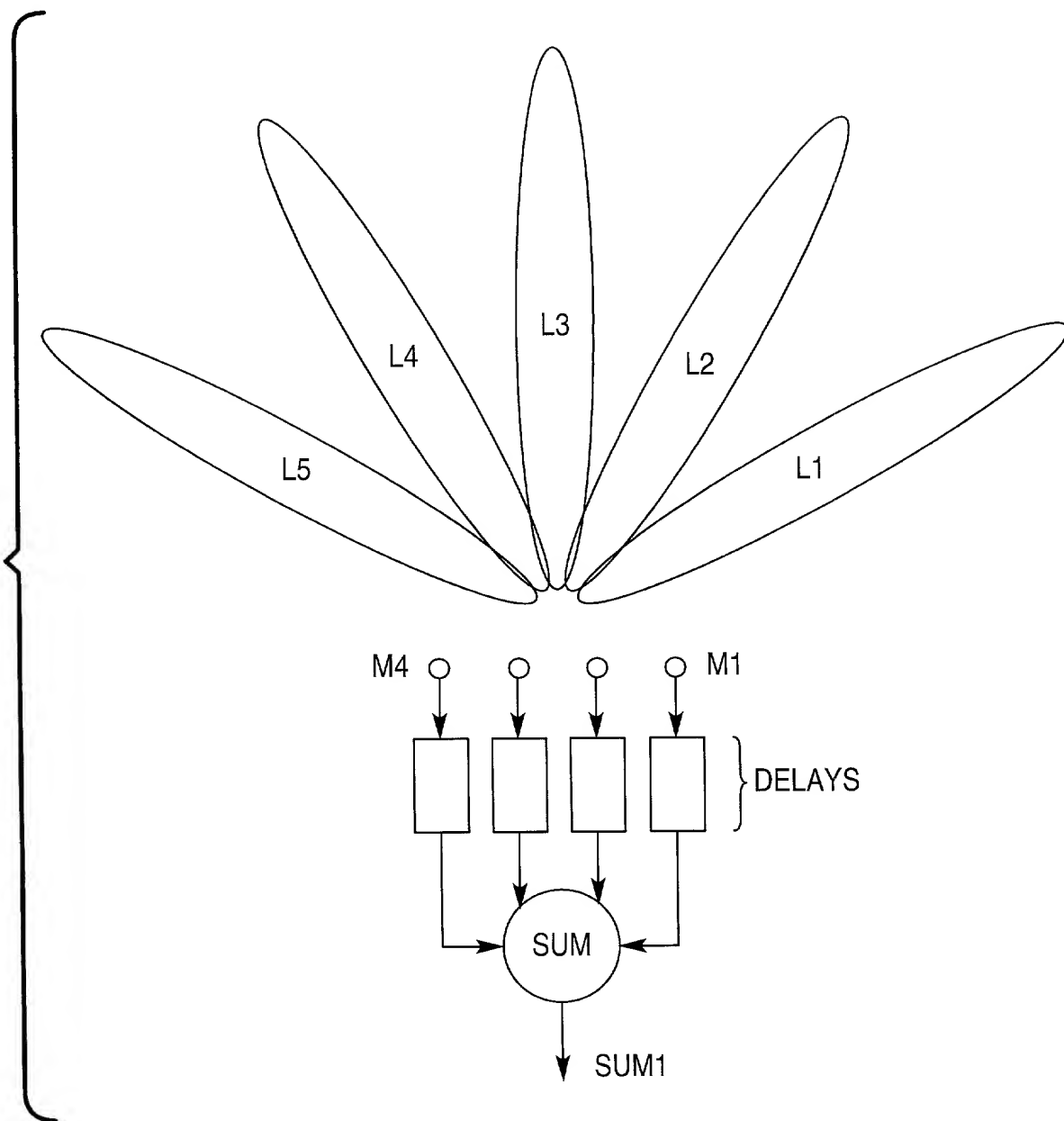


FIG. 6

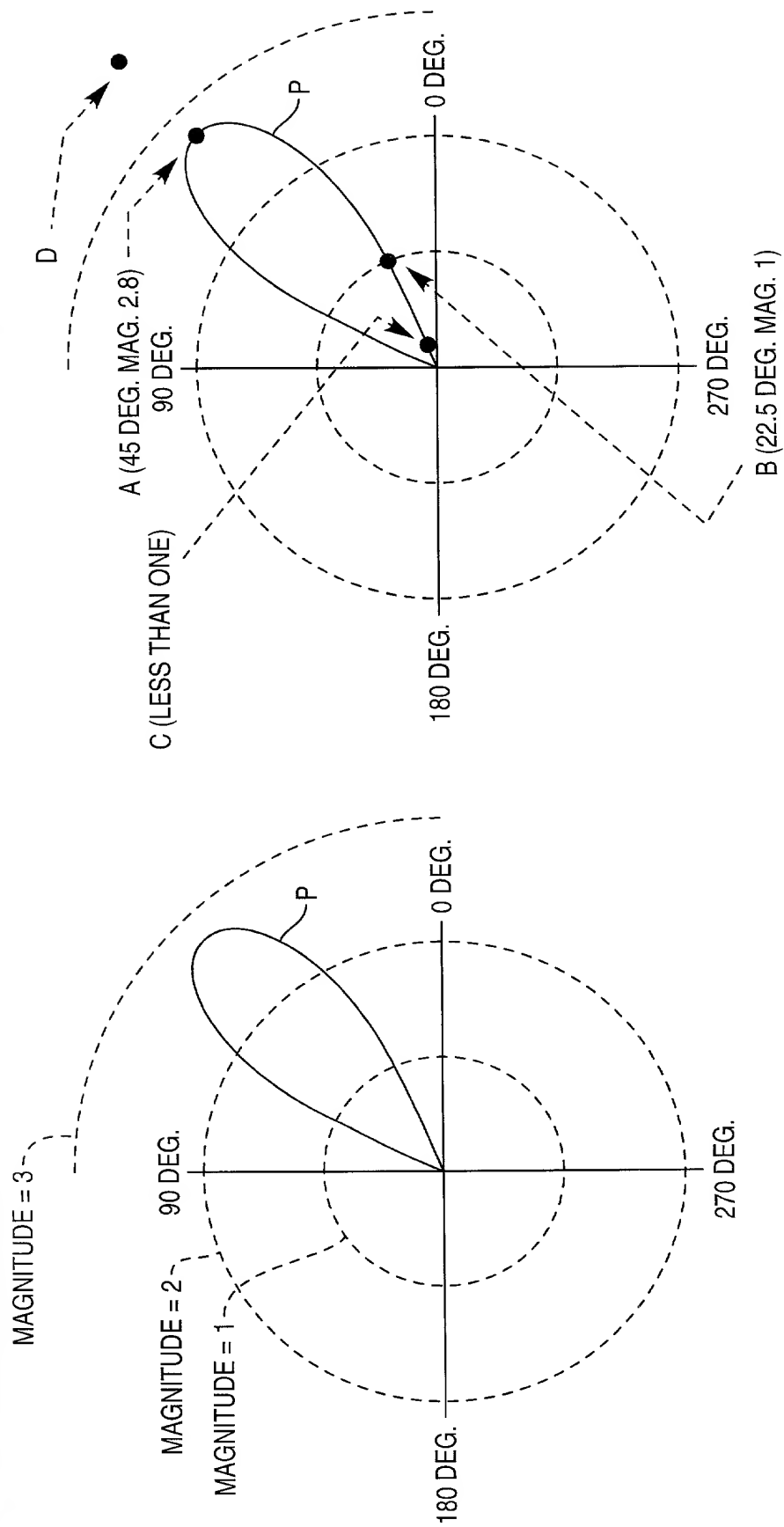


FIG. 8

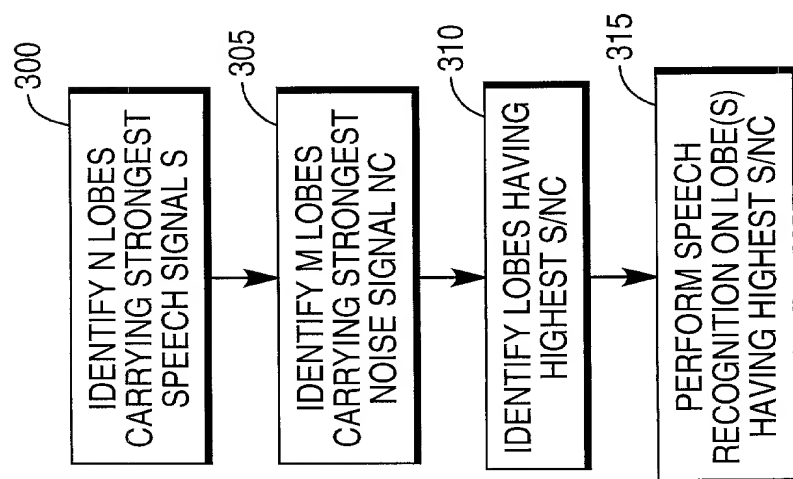
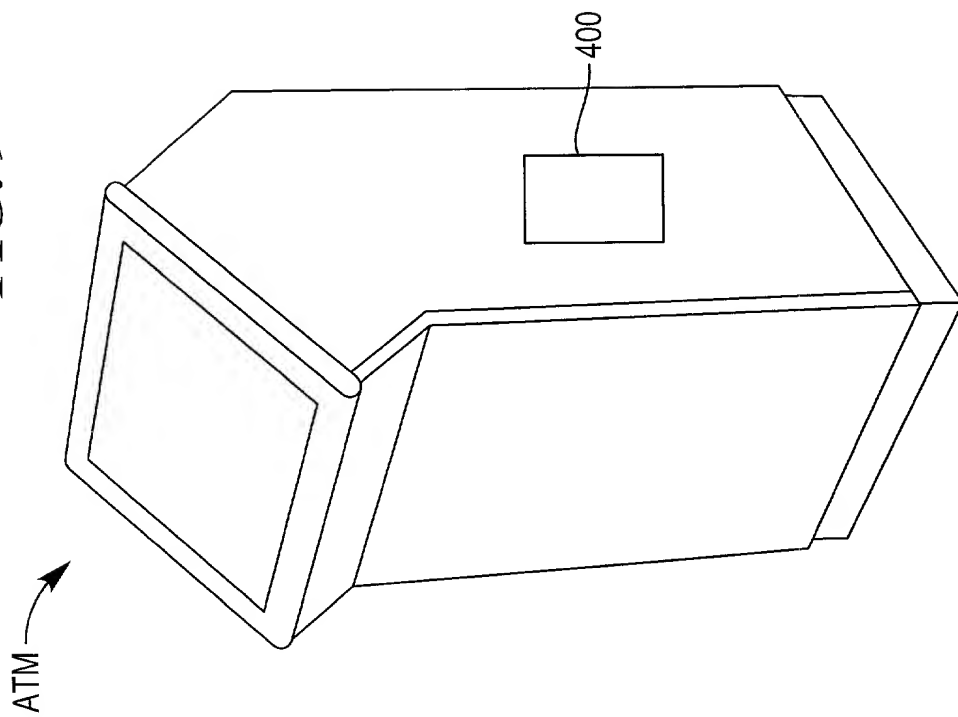


FIG. 9



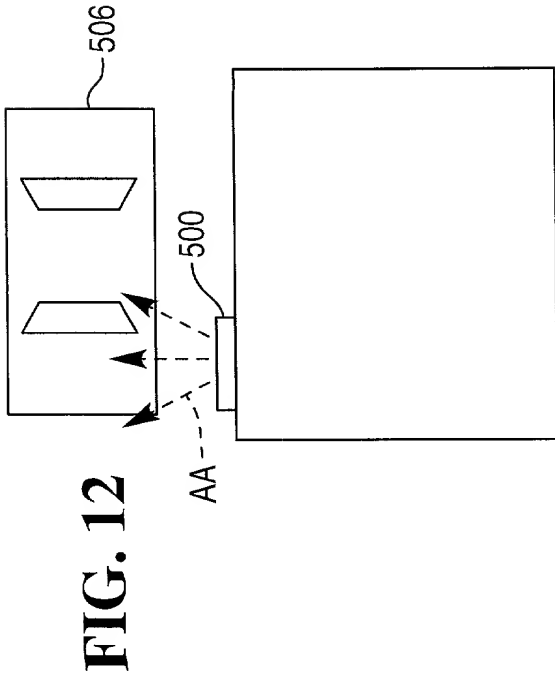


FIG. 10

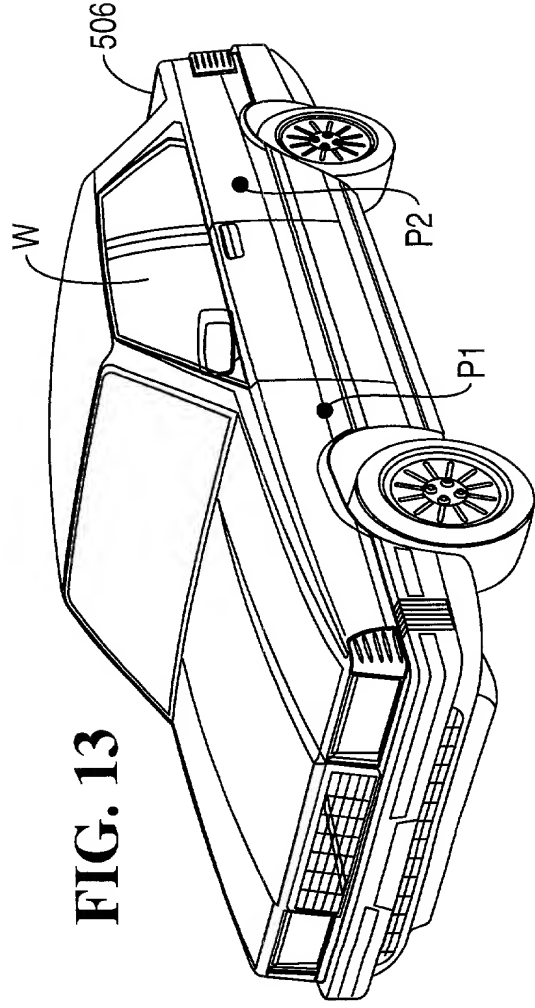
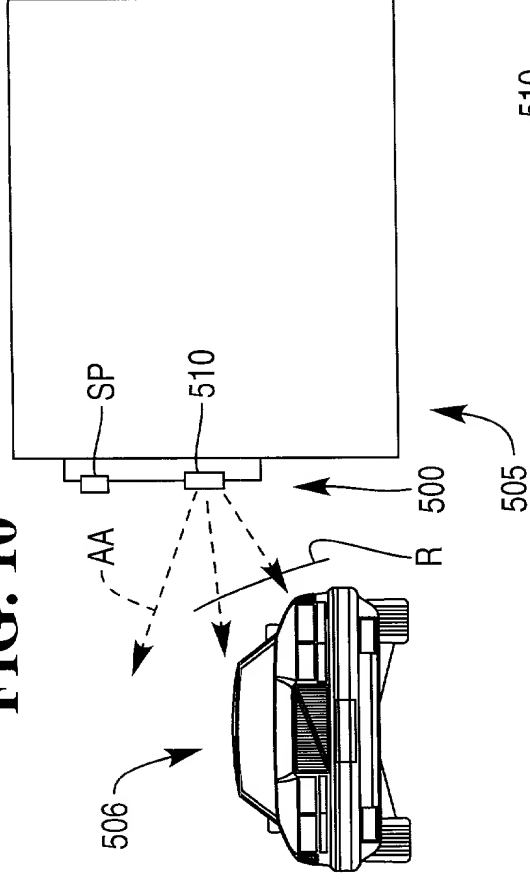


FIG. 11

